DYM-00-001 (YMEDIA.001A) REMARKS

Examiner A. Moe is thanked for the thorough examination and search of the subject Patent Application. Claims 1, 29, and 38 have been amended. Claims 2, 4-28, 30, 32-37, and 39-42 have been canceled.

All Claims are believed to be in condition for Allowance, and that is so requested.

Reconsideration of Claims 1, 3, 38, 43, 44, 47 and 48 rejected under 35 U.S.C. 103(a) as being unpatentable over Hashimoto (U.S. 4,768,085) in view of Lee et al (U.S. 6,466,265) and Ochi et al (U.S. 4,580,160) is requested based on Amended Claims 1 and 38 and on the following remarks.

Applicant has amended Claim 1 to add the limitation "wherein said readout control circuit comprises a pattern generator that is programmed by a digital command" as is shown by Amended Claim 1 below:

1. (Currently Amended) A color imaging system

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providing on-the-fly color interpolation using analog
signals to reconstruct colors during sensor readout, the
imaging system comprising:

an array of pixel sensor elements wherein at east part of the array is arranged in rows and columns;

a color filter including a plurality of color filter components organized in a predefined pattern, the color filter overlaying at least a portion of the array;

a readout control circuit coupled to the array;

an array controller coupled to the array wherein the readout control circuit and the array controller are is configured to simultaneously read out values for a group of pixel elements within a first portion of the array,

including at least two pixel elements from two different rows and two pixel elements from two different columns and to reconstruct color components for at least a first pixel sensor element and a second pixel sensor element using color information from other pixels elements within at

least the first portion of the array while the readout control circuit is reading said first portion of the array and wherein said readout control circuit comprises a pattern generator that is programmed by a digital command;

a summing amplifier to sum values of two or more said

25 values of said pixel elements; and

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a plurality of color amplifiers each corresponding to one of said color filter components wherein each said color amplifier has a programmable gain.

The added limitation corresponds to the pattern generator circuit on Fig. 12 (described on Specification page 18) and does not constitute new matter. Applicant has reviewed the cited art and does not find this additional element. In particular, Hashimoto and Lee et al and Ochi et al do not appear to teach or to suggest, separately or in combination, the pattern generator circuit used in Applicant's claimed invention as recited in Amended Claim 1. More particularly, Applicant's pattern generator 290 is supplied, or programmed, with an initial pattern by the digital signal INIT PATTERN 292. Applicant has carefully reviewed the cited art of Hashimoto and Lee et al and Ochi et al and has concluded that none of this art appears to teach or to suggest, separately or in combination, the pattern generator circuit that is programmed with the digital signal. Therefore, Applicant believes that Amended Claim 1 now contains subject matter that is neither taught nor suggested in the prior art and should, therefore, not be rejected under 35 U.S.C. 103(a). In addition, Claims 3, 43, and 44 represent patentably distinct, further limitations on Claim 1 and should be in condition for allowance if the rejection of Claim 1 is removed.

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Applicant has also amended Claim 38 in similar fashion to Claim 1. Amended Claim 38 is shown below:

- 38. (Currently Amended) A color imager comprising:
- a first light sensor which generates a first analog output signal related to the amount of a first color of light sensed;
- a second light sensor which generates a second analog output signal related to the amount of a first color of light sensed;
  - a third light sensor which generates a third analog output signal related to the amount of a second color of light sensed;
  - a fourth light sensor which generates a fourth analog output signal related to the amount of a third color of light sensed;
- a circuit configured to read out the first, second,

  third, and fourth analog values at the same time wherein said circuit comprises a pattern generator that is programmed by a digital command; and

an interpolation circuit configured to receive said first output signal and said second output signal, wherein

- said interpolation circuit provides and interpolation signal on the fly based on at least said first analog output signal and said second analog output signal;
  - a summing amplifier to sum two or more said analog values; and
- a plurality of color amplifiers each corresponding to one of said colors of light wherein each said color amplifier has a programmable gain.

The added limitation corresponds to the pattern generator circuit on Fig. 12 (described on Specification page 18) and does not constitute new matter. Applicant has reviewed the cited art and does not find this additional element. In particular, Hashimoto and Lee et al and Ochi et al do not appear to teach or to suggest, separately or in combination, the pattern generator circuit used in Applicant's claimed invention as recited in Amended Claim 1. More particularly, Applicant's pattern generator 290 is supplied, or programmed, with an initial pattern by the digital signal INIT PATTERN 292. Applicant has carefully reviewed the cited art of Hashimoto and Lee et al and Ochi et al and has concluded that none of this art appears to teach or to suggest, separately or in combination, the pattern generator circuit that is programmed with the digital signal. Therefore, Applicant respectfully requests that the rejection

DYM-00-001 (YMEDIA.001A) under 35 U.S.C. 103(a) of Amended Claim 38 be removed. In addition, Claims 47 and 48 represent patentably distinct, further limitations on Claim 38 and should be in condition for allowance if the rejection of Claim 38 is removed.

Reconsideration of Claims 1, 3, 38, 43, 44, 47 and 48 rejected under 35 U.S.C. 103(a) as being unpatentable over Hashimoto (U.S. 4,768,085) in view of Lee et al (U.S. 6,466,265) and Ochi et al (U.S. 4,580,160) is requested based on Amended Claims 1 and 38 and on the above remarks.

Reconsideration of Claim 45 rejected under 35 U.S.C. 103(a) as being unpatentable over Hashimoto (U.S. 4,768,085) in view of Lee et al (U.S. 6,466,265) and Ochi et al (U.S. 4,580,160) and further in view of Wilder et al (U.S. 5,262,871) is requested based on Amended Claim 1 and on the following remarks.

As discussed above, Amended Claim 1 now contains the additional limitation, "wherein said readout control circuit comprises a pattern generator that is programmed by a digital command." In particular, Hashimoto and Lee et al and Ochi et al and Wilder et al do not appear to teach or to suggest, separately or in combination, the pattern generator circuit that is programmed with the digital signal as recited in Applicant's

DYM-00-001 (YMEDIA.001A) claimed invention in Amended Claim 1. Therefore, Applicant believes that Amended Claim 1 now contains subject matter that is neither taught nor suggested in the prior art and should, therefore, not be rejected under 35 U.S.C. 103(a). In addition, Claim 45 a represent patentably distinct, further limitation on Claim 1 and should be in condition for allowance if Claim 1 is not rejected.

Reconsideration of Claim 45 rejected under 35 U.S.C. 103(a) as being unpatentable over Hashimoto (U.S. 4,768,085) in view of Lee et al (U.S. 6,466,265) and Ochi et al (U.S. 4,580,160) and further in view of Wilder et al (U.S. 5,262,871) is requested based on Amended Claim 1 and on the above remarks.

Reconsideration of Claim 29, 31, and 46 rejected under 35 U.S.C. 103(a) as being unpatentable over Maenaka et al (U.S. 5,555,023) in view of Lee et al (U.S. 6,466,265) is requested based on Amended Claim 29 and on the following remarks.

Applicant has amended Claim 29 to add the aforementioned limitation. Amended Claim 29 is shown below:

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29. (Currently Amended) A method of interpolating color components of an array of pixel sensor elements, said method comprising:

reading a first rectangular portion of an array of

pixel sensor elements simultaneously, wherein the first
rectangular portion includes pixel sensor elements from at
least two array columns and two array rows;

reading a second rectangular portion of the array of pixel sensor elements, wherein the second portion partly overlaps said first portion and wherein said reading of said first and second rectangular portions is controlled by a pattern generator that is programmed by a digital command; and

reconstructing color components using interpolation

15 for at least a third portion of the array while said third portion of the array is being read wherein said array of pixel sensor elements comprises CMOS sensors and wherein said interpolation comprises summing values of two or more said pixel sensor elements.

Applicant has reviewed the cited art and does not find this additional limitation. In particular, Maenaka and Lee et al do not appear to teach or to suggest, separately or in combination, the pattern generator circuit that is programmed with the

DYM-00-001 (YMEDIA.001A) digital signal as recited in Applicant's claimed invention in Amended Claim 29. Therefore, Applicant believes that Amended Claim 29 now contains subject matter that is neither taught nor suggested in the prior art and should, therefore, not be rejected under 35 U.S.C. 103(a). In addition, Claims 31 and 46 represent patentably distinct, further limitations on Claim 29 and should be in condition for allowance if the rejection of Claim 29 is removed.

Reconsideration of Claim 29, 31, and 46 rejected under 35 U.S.C. 103(a) as being unpatentable over Maenaka et al (U.S. 5,555,023) in view of Lee et al (U.S. 6,466,265) is requested based on Amended Claim 29 and on the above remarks.

Applicants have reviewed the prior art made of record and not relied upon and have discussed their impact on the present invention above.

Allowance of all Claims is requested. It is also requested that should the Examiner not find that the Claims are now Allowable that the Examiner call the undersigned at 989-894-4392 to overcome any problems preventing allowance.

Respectfully submitted,

Stephen B. Ackerman, Reg. No. 37,761